

Model Name: GA-H77TN-CC

SHEET	TITLE
01	COVER SHEET
02	BLOCK DIAGRAM
03	POWER SEQUENCE
04	Processor HOST
05	Processor DDRIII
06	Processor PWR
07	Processor GND
08	DDRIII CHANNEL A & B
09	MINI PCIE ,eDP
10	HDMI, VGA CON
11	PCH DMI, PCI, PCIE, USB
12	PCH HOST,LPC, SPI, PM, HDA
13	PCH HOST, SATA
14	PCH NVRAM, FDI, DISPLAY
15	PCH CLK BUFFER AND CLK GEN
16	PCH PWR,GND
17	SIO IT8773 &FRONT PANEL
18	FRONT & REAL USB
19	VCC_CORE & AXG
20	SWITCH POWER
21	LINNER POWER
22	FAN CONTROL
23	HDA CODEC ALC887
24	AUDIO JACK
25	RTL8111E-VL/RTL8105E-H
26	USB30 ASM1042
27	REALTEK RTD2136

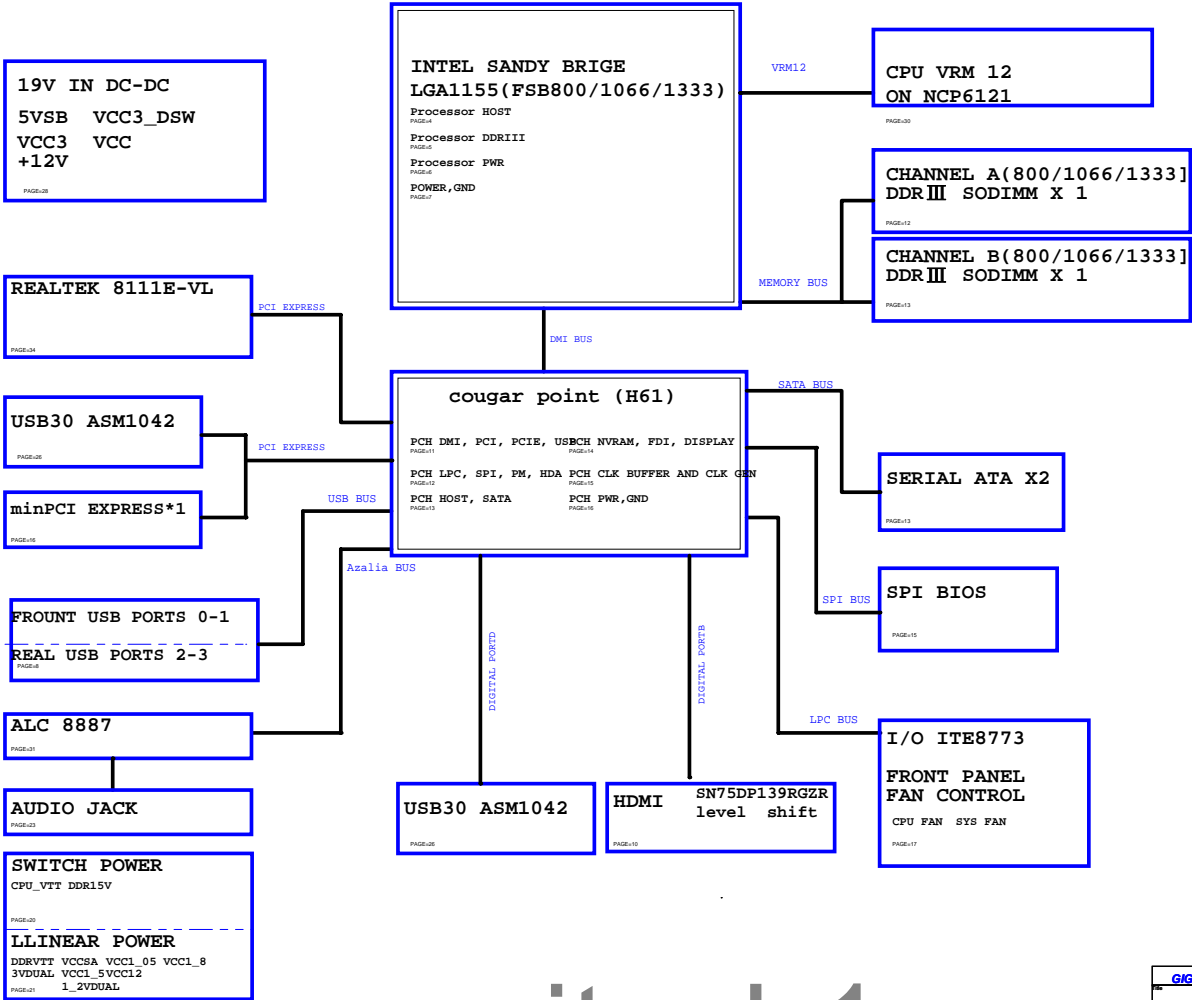
Revision 1.1

SHEET	TITLE
28	19V-IN DC to DC
29	BOM & PCB MODIFY HISTORY
30	
31	
32	
33	

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GIGABYTE TECHNOLOGIES, INC.			
Title			
Cover Sheet			
Size	Document Number		Rev
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BLOCK DIAGRAM

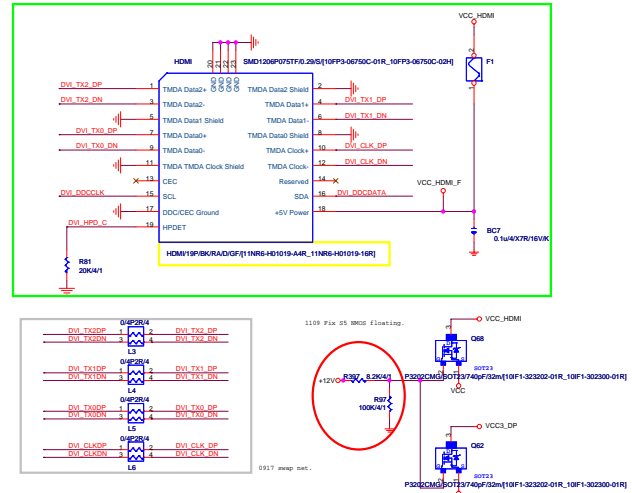
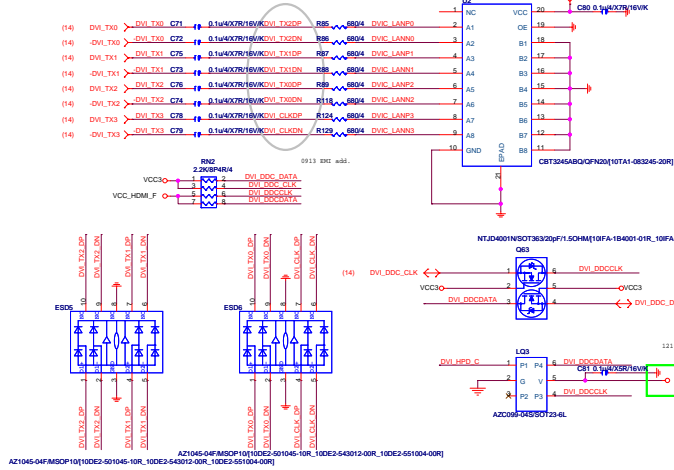


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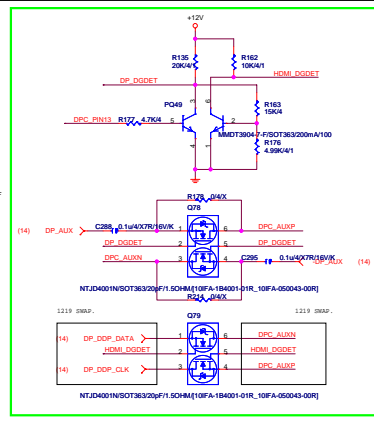
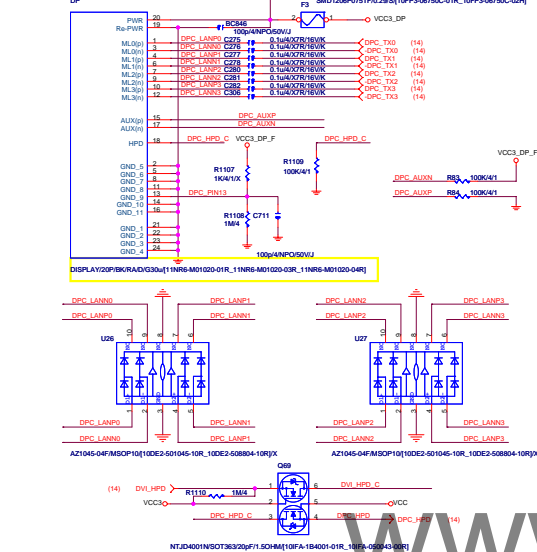
GIGABYTE TECHNOLOGIES, INC.			
POWER SEQUENCE			
Doc Control	Document Number	GA-H77TN-CC	Rev 1.1
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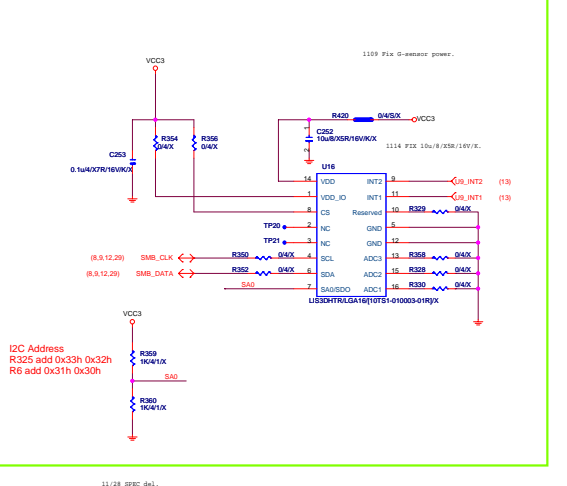
HDMI



DP



G-sensor

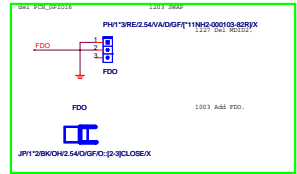


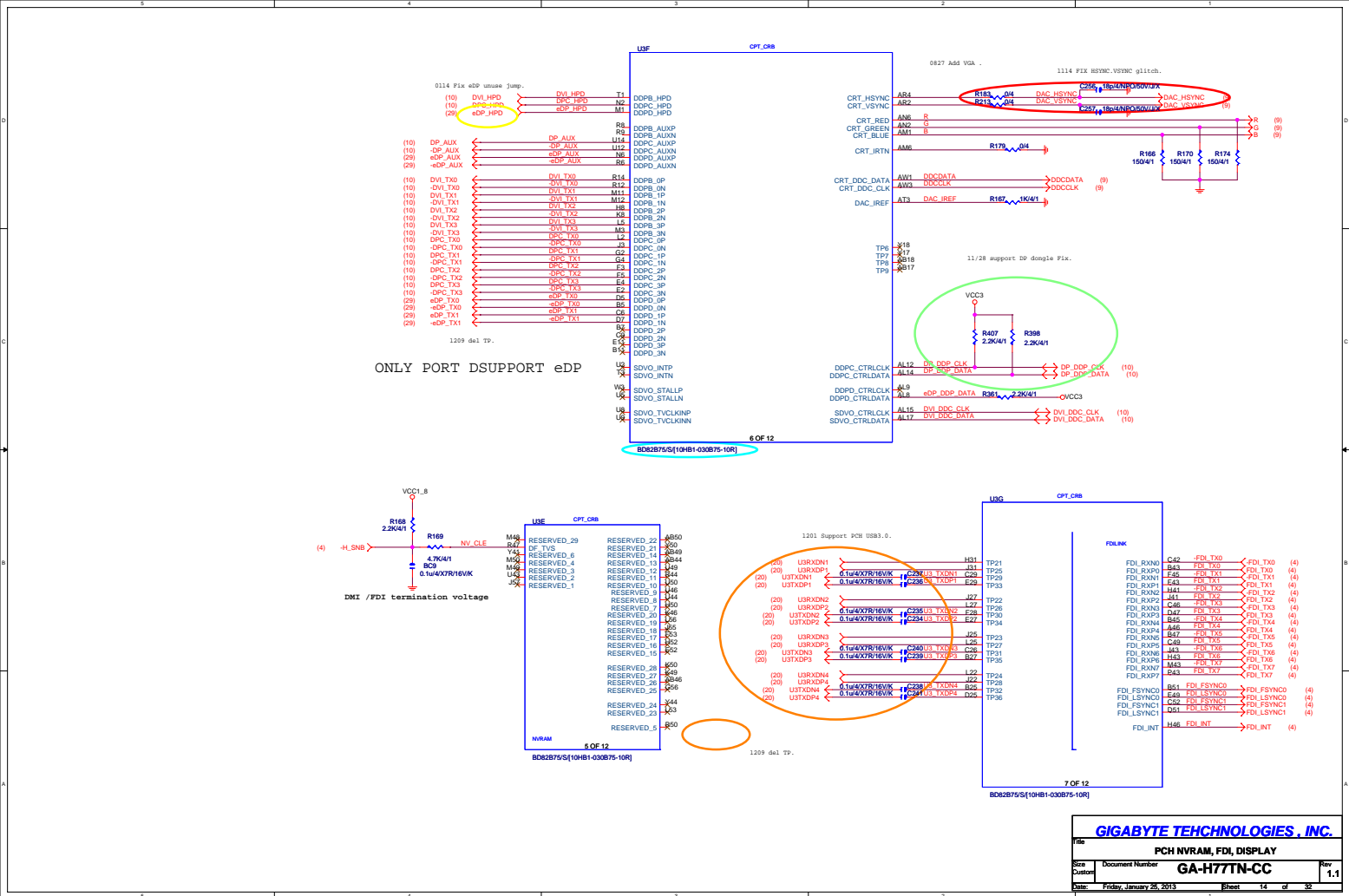
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GIGABYTE TECHNOLOGIES, INC.			
Title: PCH DMI, PCI, PCIE, USB			
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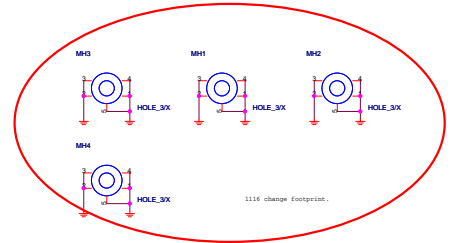
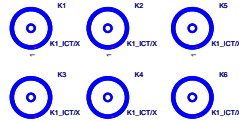
1229 No external pull up/down resistors required. Signals driven by PCI



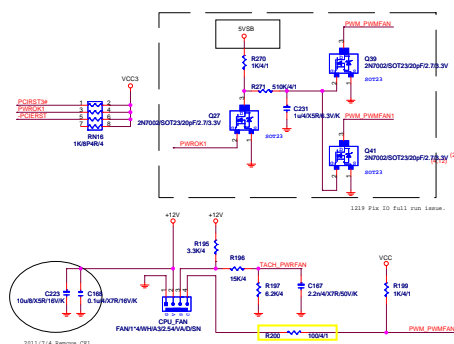




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Symbol	value	Description
JP4	1	K8 power sequence function is disabled
Pin 126	0	K8 power sequence function is enabled
	11	The default value of EC Index 15h16h17h is 80h
JP3 & JP5	10	The default value of EC Index 15h16h17h is FFFh(an off)
Pin 124 & 126	01	The default value of EC Index 15h16h17h is 00h(Fan full speed)
	00	The default value of EC Index 15h16h17h is 40h
JP2	0	Disable WDT to rest PWROK
Pin122	1	Enable WDT to rest PWROK

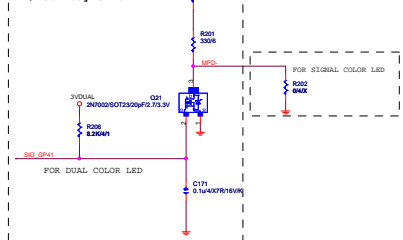


LATO SPEC.

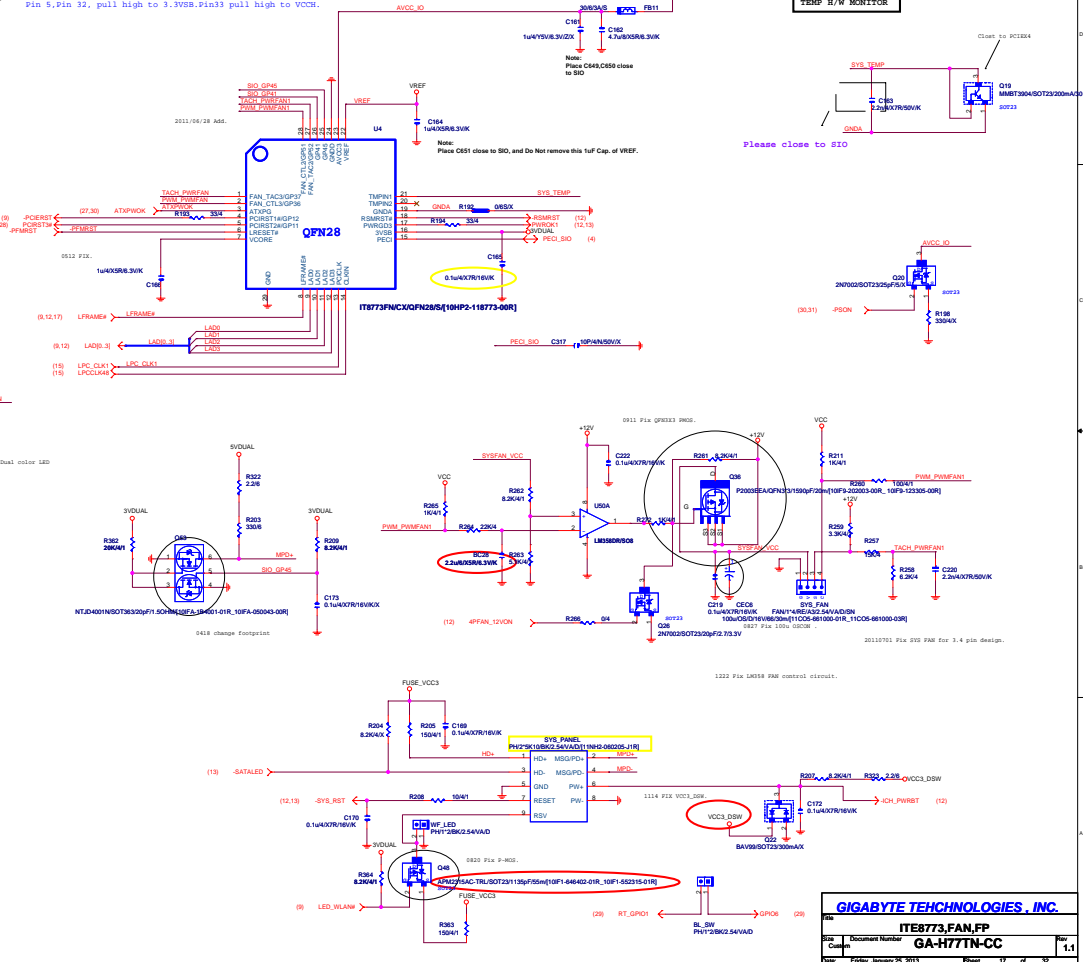
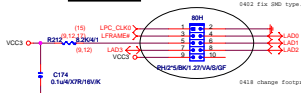
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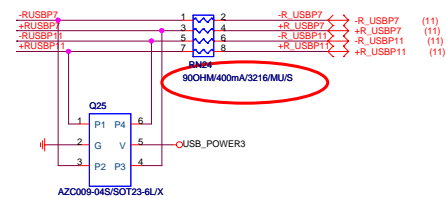
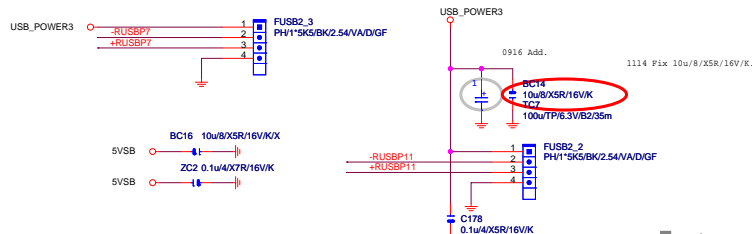
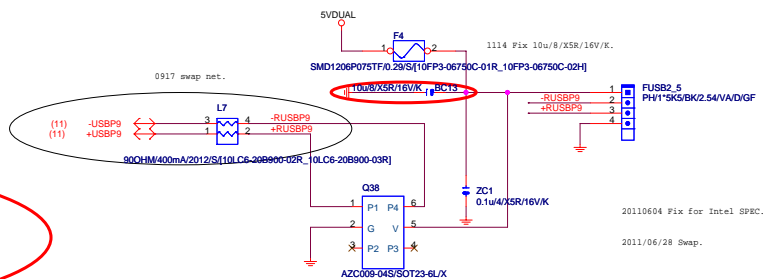
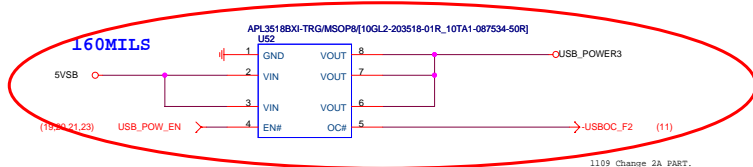
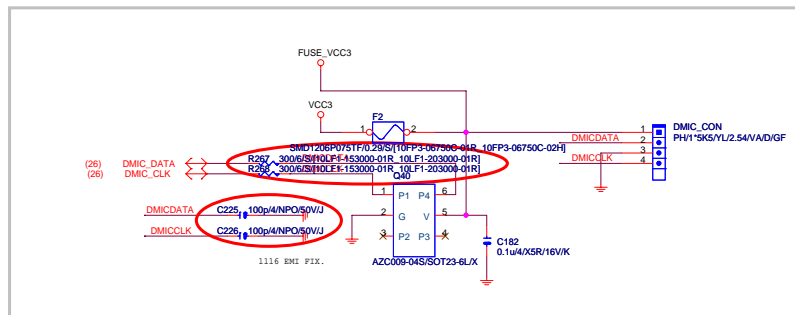
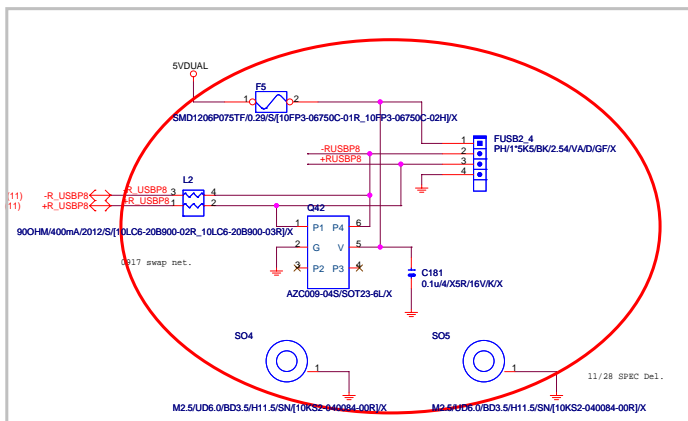
| AIO SPEC.
| S0/S1: Green
| S3: Orange.
| S4/S5: Stay off.

```



80H

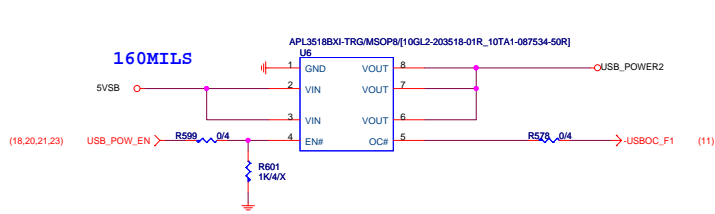




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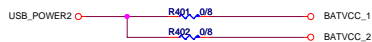
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FRONT & REAL USB			
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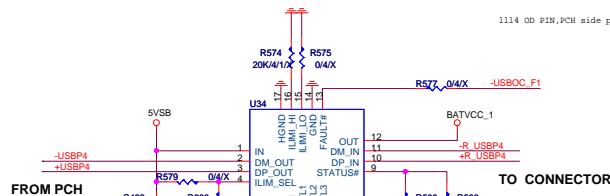
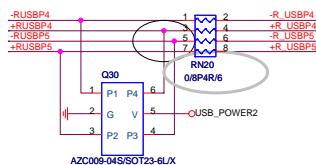
MODE SWITCH

FROM PCH



USB MODE	R404.R406.R401.R402
CHARGER MODE	U34.U35

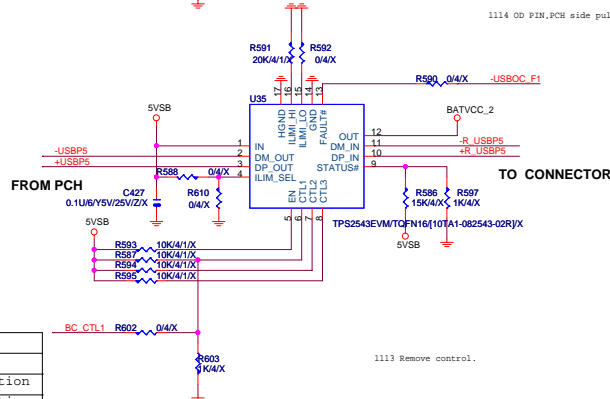
	CTL1	CTL2	CTL3	ILIM_SEL	MODE
S0	1	1	1	1	CDP, load detection
S3	0	1	1	1	Auto mode, no load detection
S4.S5(non ERP)	0	1	1	1	Auto mode, no load detection



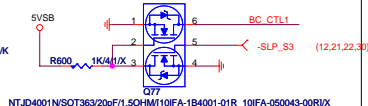
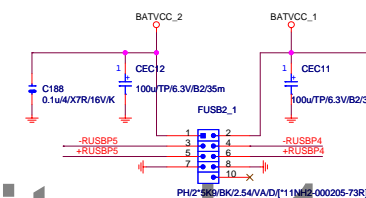
1114 OD PIN,PCH side pull high.

1113 Remove control.

1114 OD PIN,PCH side pull high.



1113 Remove control.

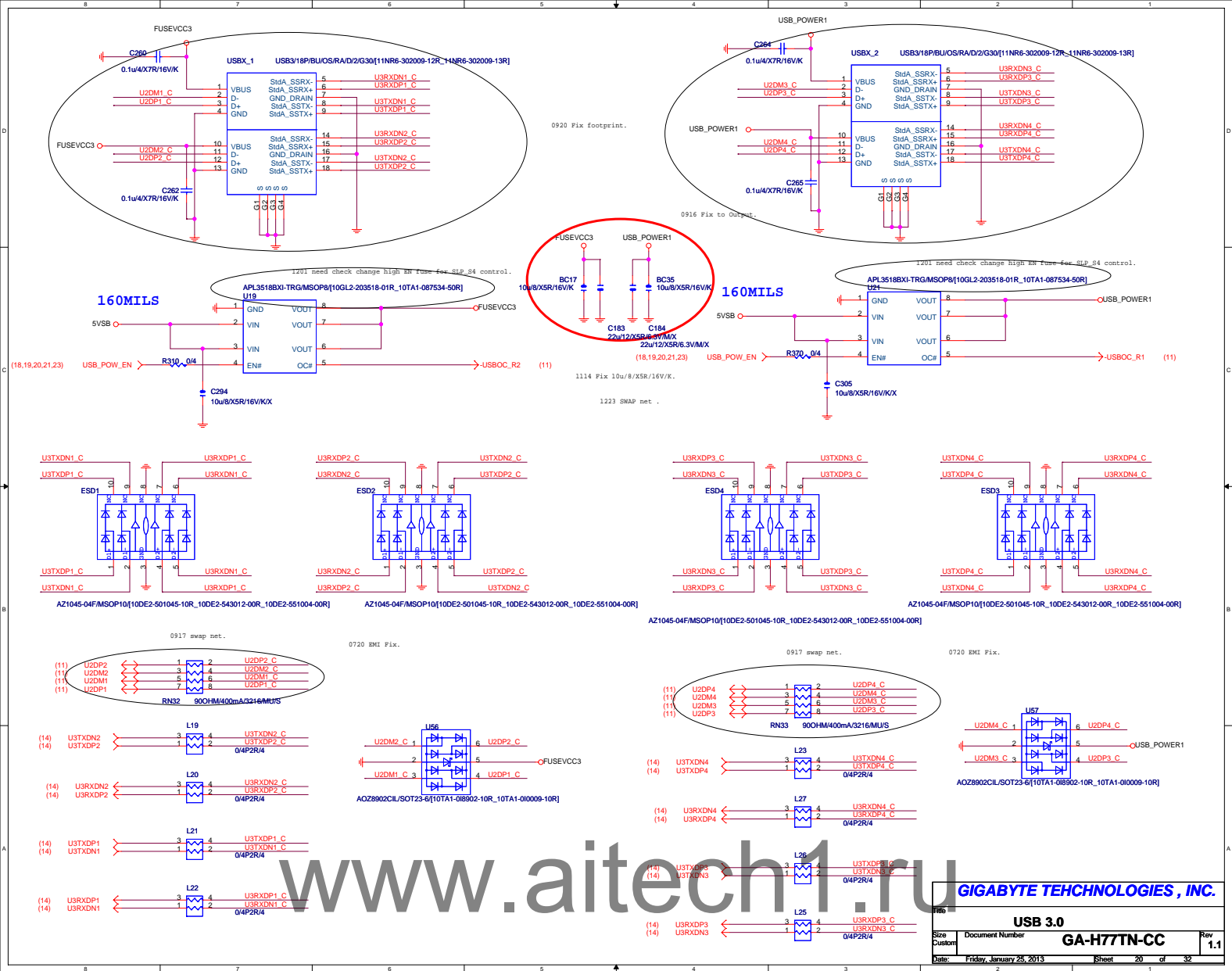


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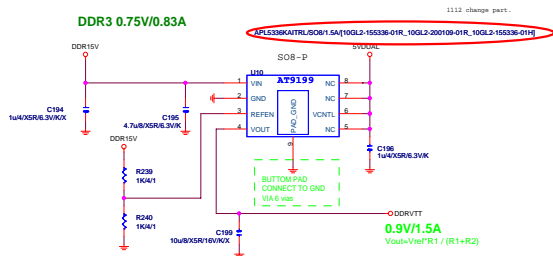
GIGABYTE TEHCHNOLOGIES, INC.

F_USB CHARGER			
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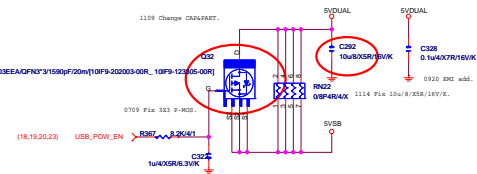


DDRVTT

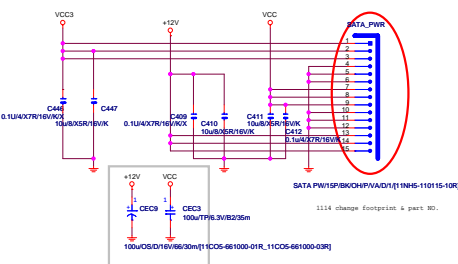
DDR3 0.75V/0.83A



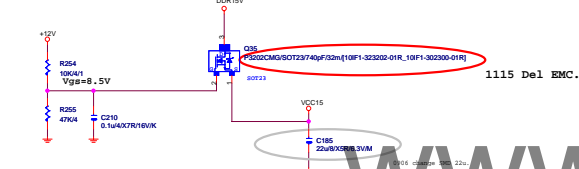
5VDUAL



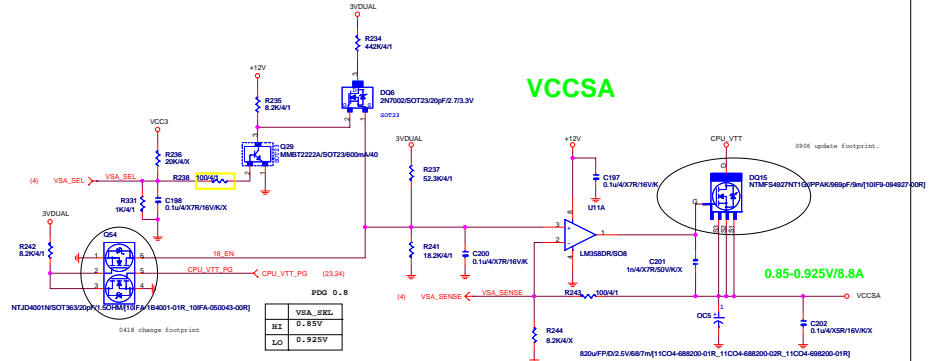
POWER CONNECTOR



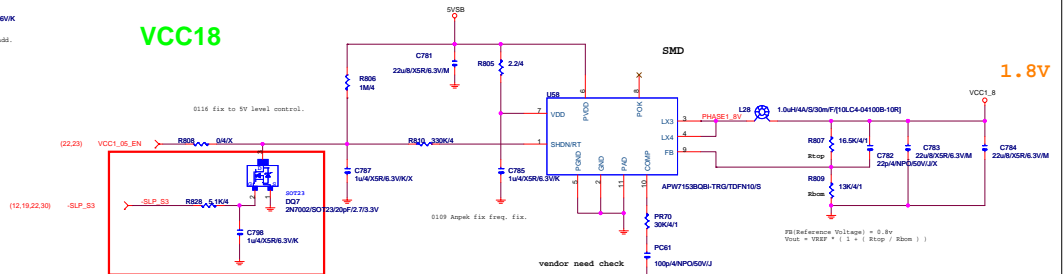
VCC15&VCC12



VCCSA

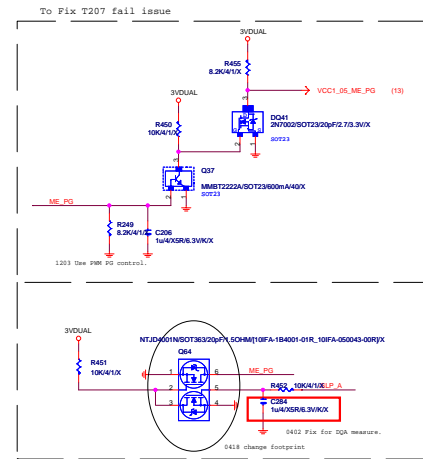


VCC18



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GIGABYTE			
Rev	LINNER-POWER-1		
Docu	GA-H77N-CC		
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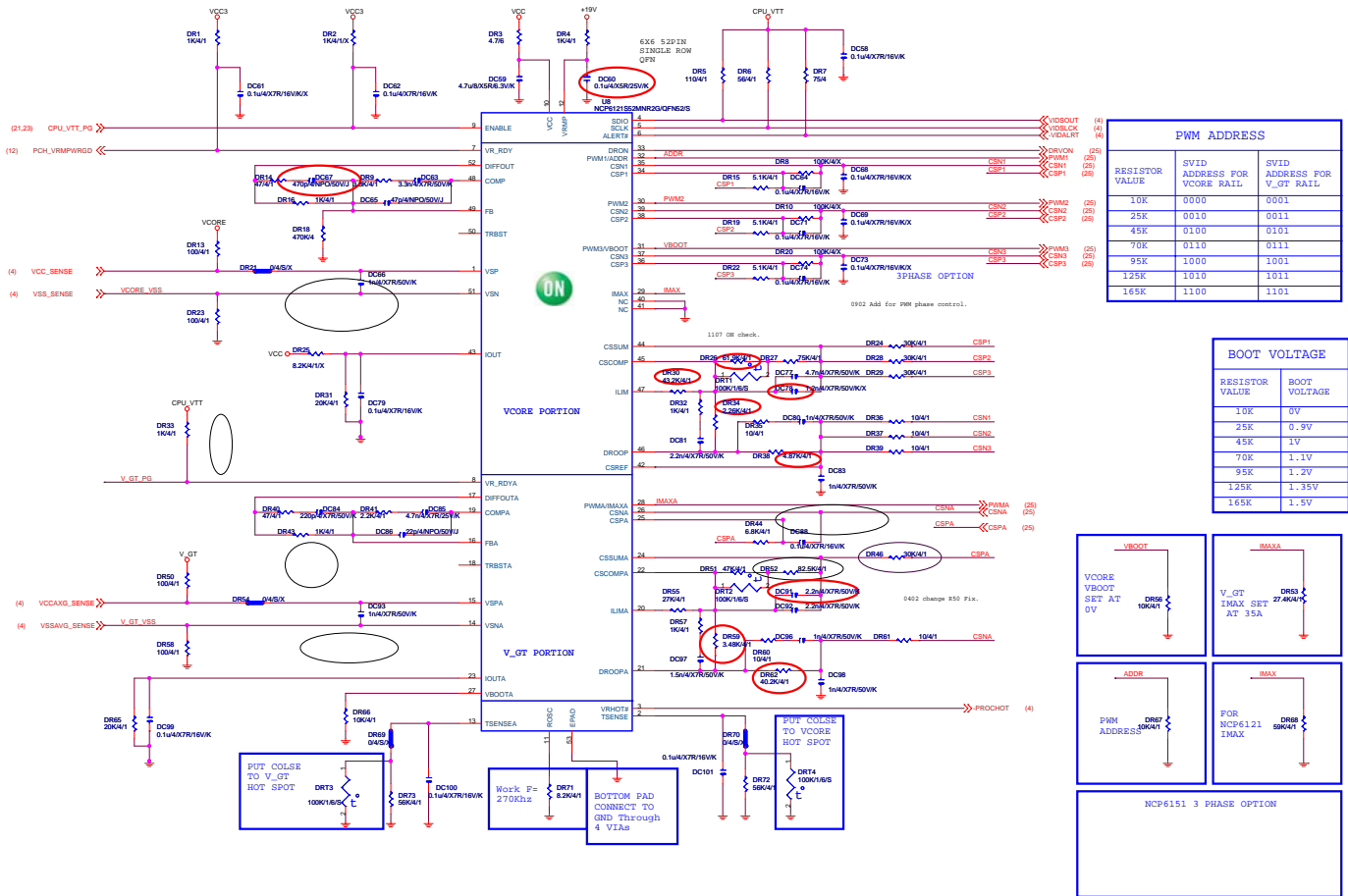


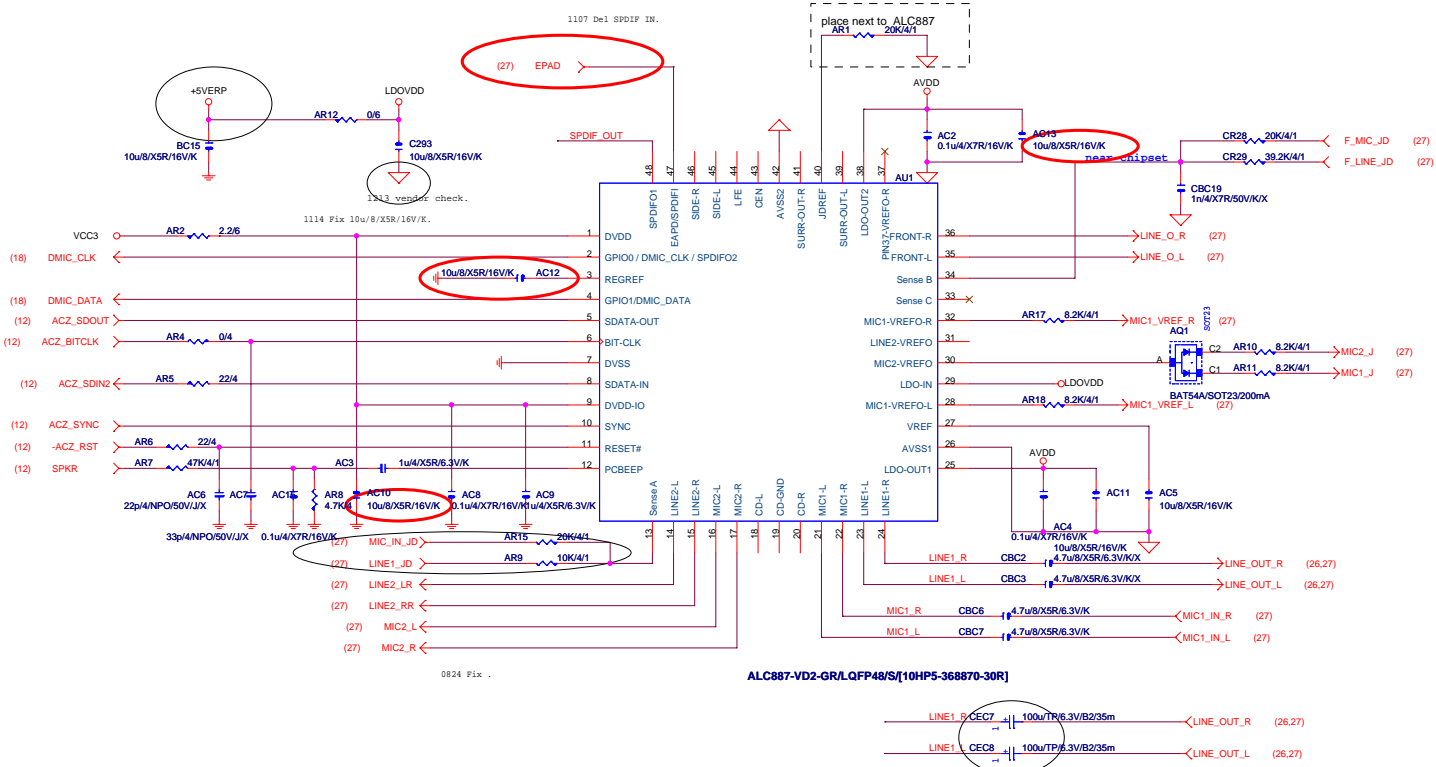
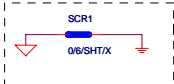
Note.

Non AMT model:
DQ8.R829.C799.Q33.R448.R227 remove.
RN23.R449.R226 use.

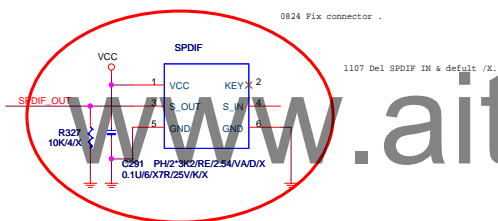
AMT model:
RN23.R226.R449 remove.
DQ8.R829.C799.Q333.R448.R227 use.

Intel SugarBay VR12 POWER CKT - 4+1 PHASE





ALC887-VD2-GR/LQFP48/S[10HP5-368870-30R]



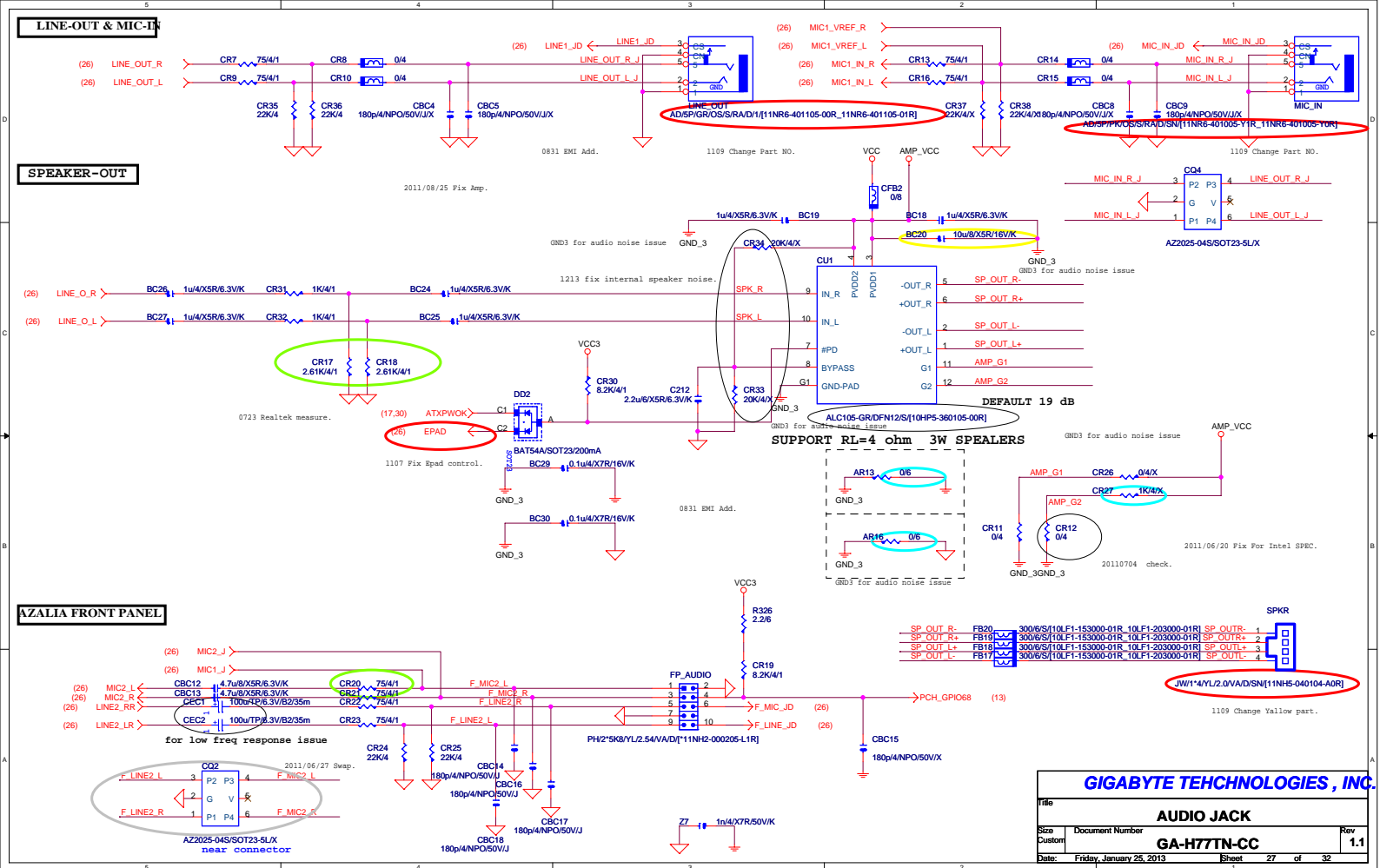
0824 Pin connector .

1107 Del SPDIF IN & default /X.

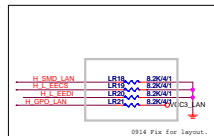
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Size	Document Number	Rev	
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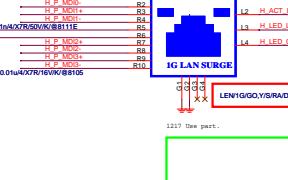
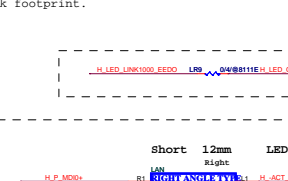
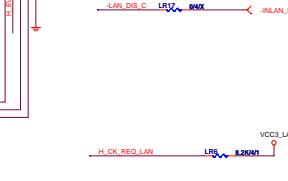
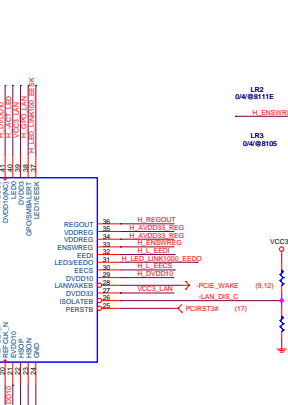
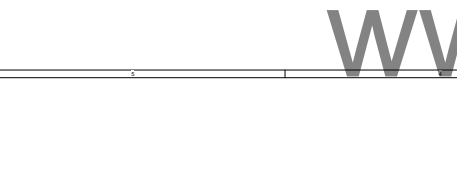
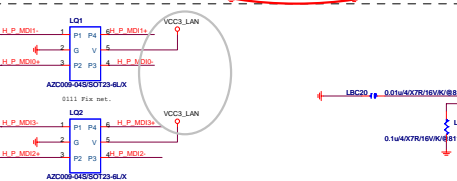
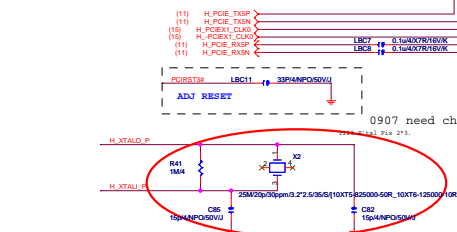
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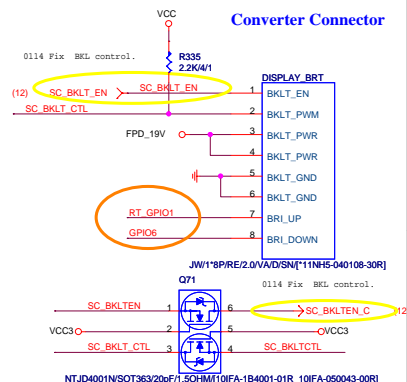
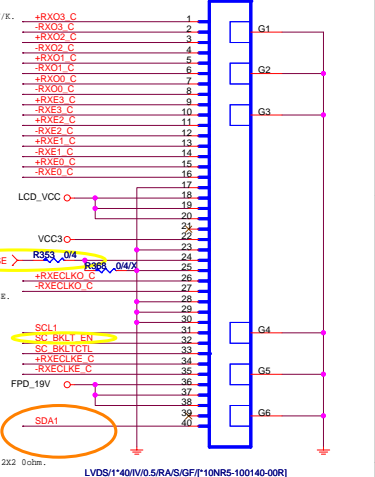
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- 014 Pin Fix for layout:
- When using ESP8060 only without ASP function.
Mount L822 10K for 93C56/64.
Mount L821 10K for 93C46.
Do-mount L88 for not support ASP.
 - When using ESP8060(Only 93C56/64) with ASP function.
Mount L822 1K for 93C56/64.
Mount L88 for support ASP.
 - When using ESP8060/8222 Patch without ASP function.
Mount L822 10K for SMD_LAN.
Do-mount L88 for not support ASP.
- Note: Support ASP 93C56 is necessary and SMD_LAN must pull-high 10K if not support please RC.



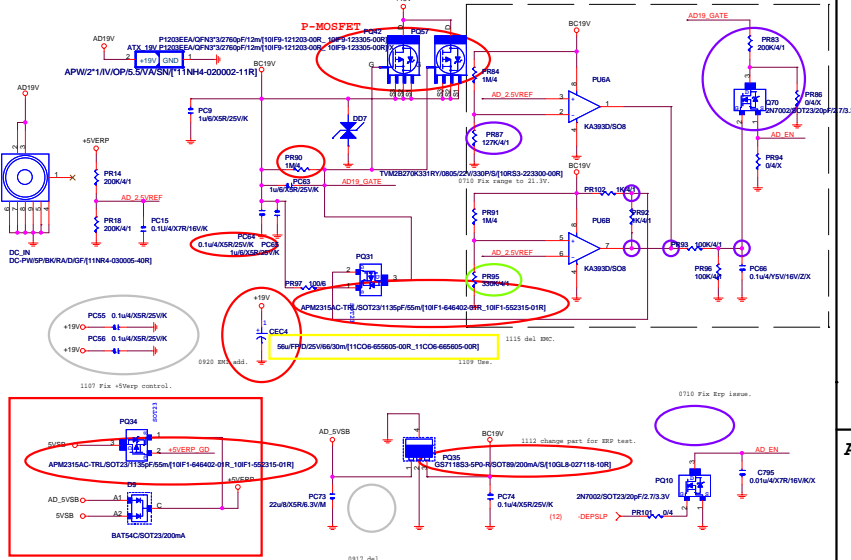
0114 Add BKL control.



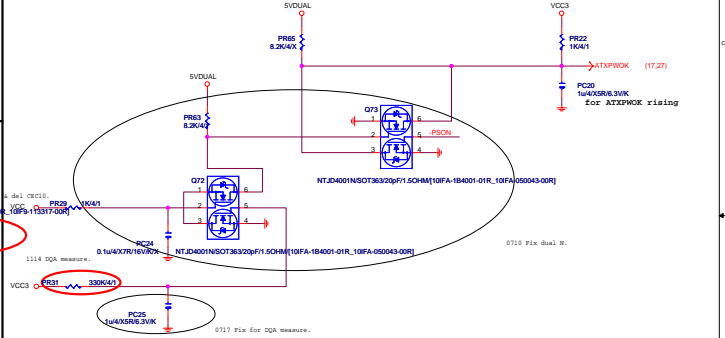
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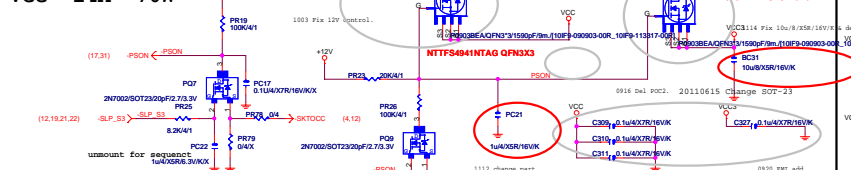
AD19V



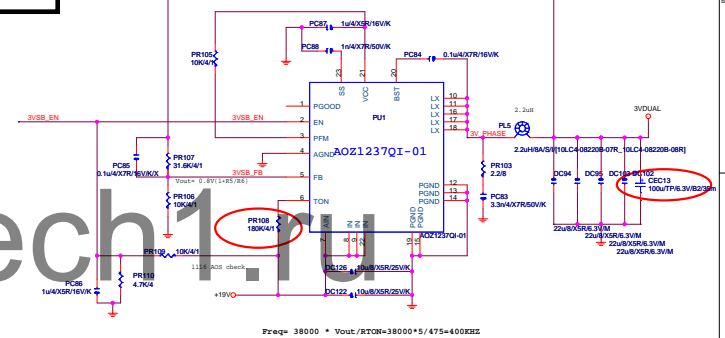
ATX PPOWER GOOD



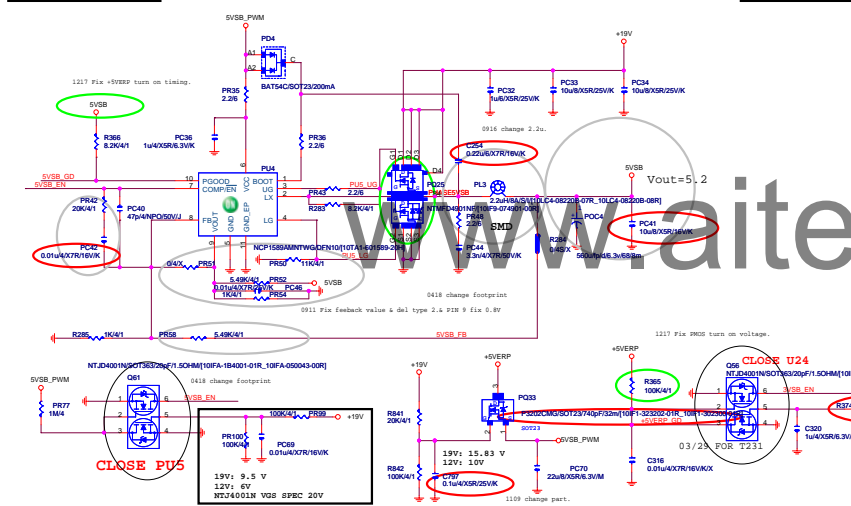
VCC&VCC3



VCC3_DSW



5VSB
Voltage level : 5V
Max current : 13A



GIGABYTE TECHNI			
19V IN DC-DC			
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Version:0.1

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